**Mireille MOUIS** is co-ordinating research activities at IMEP-LaHC, Grenoble, France, on ultimate CMOS architectures, and nanoscale devices that may extend performance and functionality of future integrated circuits. The group addresses device design and simulation as well as advanced electrical and near-field characterisation. It has developed strong collaborations with academic, pre-industrial and industrial partners. Mireille Mouis has been participating or managing several research projects at the regional, national or European level, some of them on-going. She authored or co-authored 8 chapter books and more than 220 papers in international conferences and refereed journals. She is the director of the research Federation on Micro Nano Technologies (FMNT) in Grenoble. She is serving as Electron Device Society chapter chair for IEEE France section.